

FIG. 1

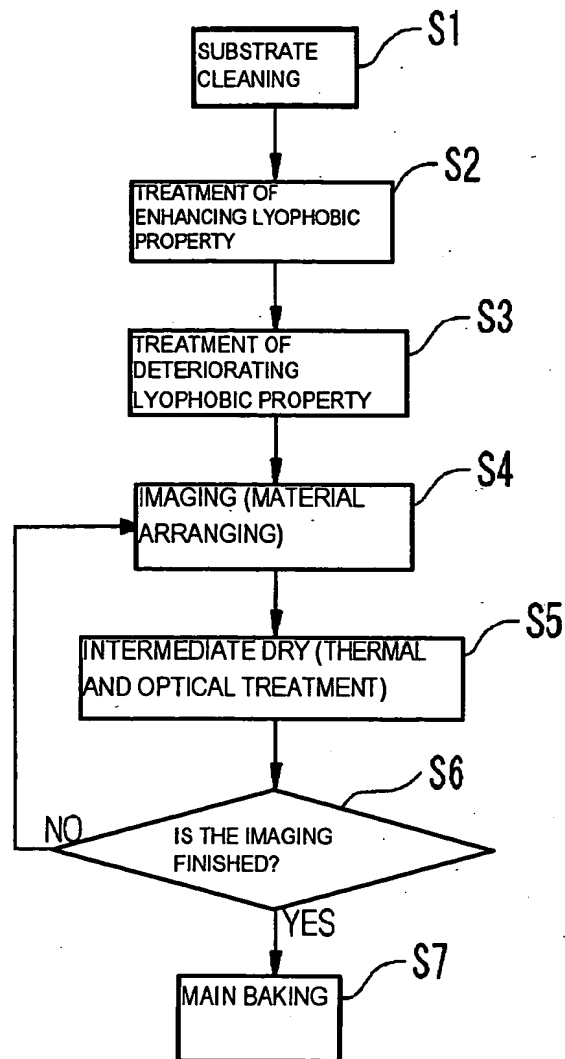


FIG. 2 A

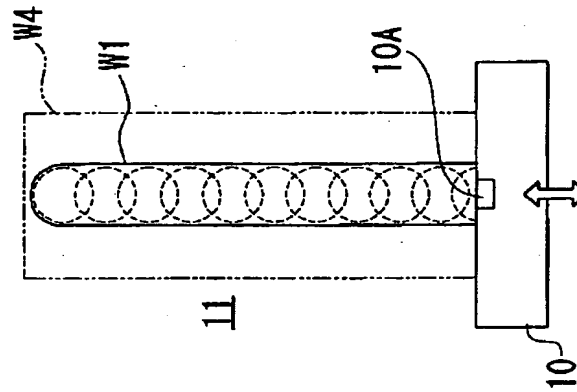


FIG. 2 B

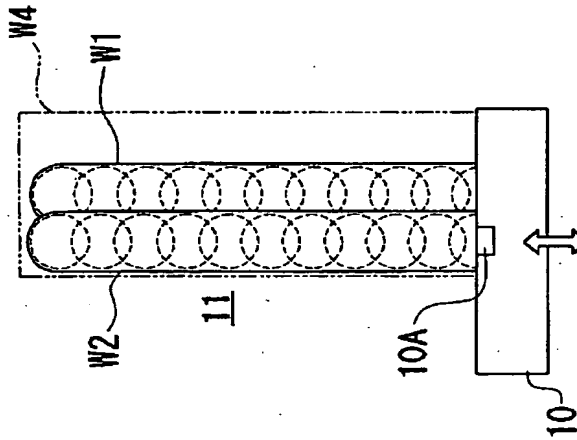


FIG. 2 C

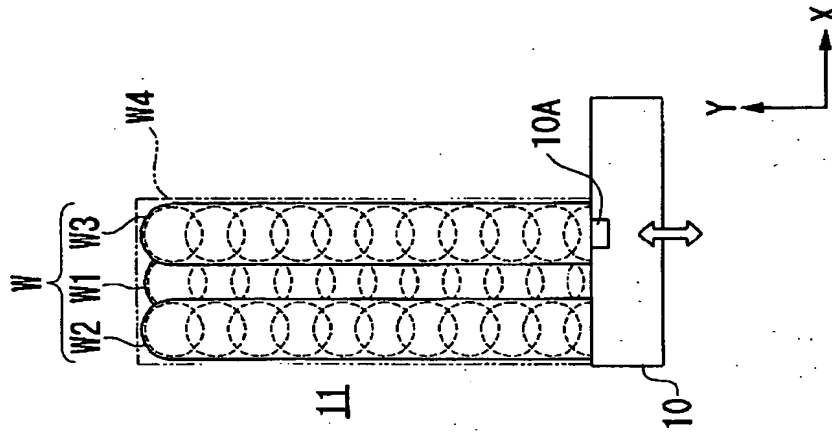


FIG. 3 A

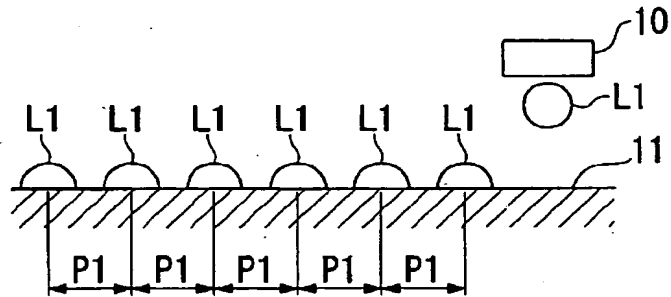


FIG. 3 B

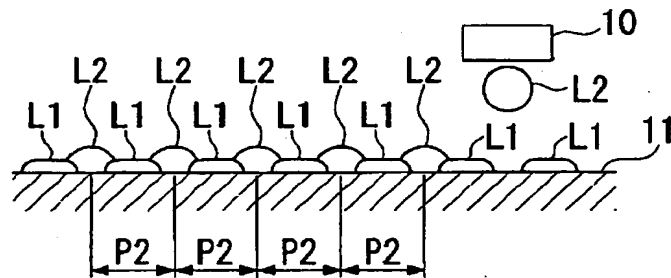


FIG. 3 C



FIG. 4 A

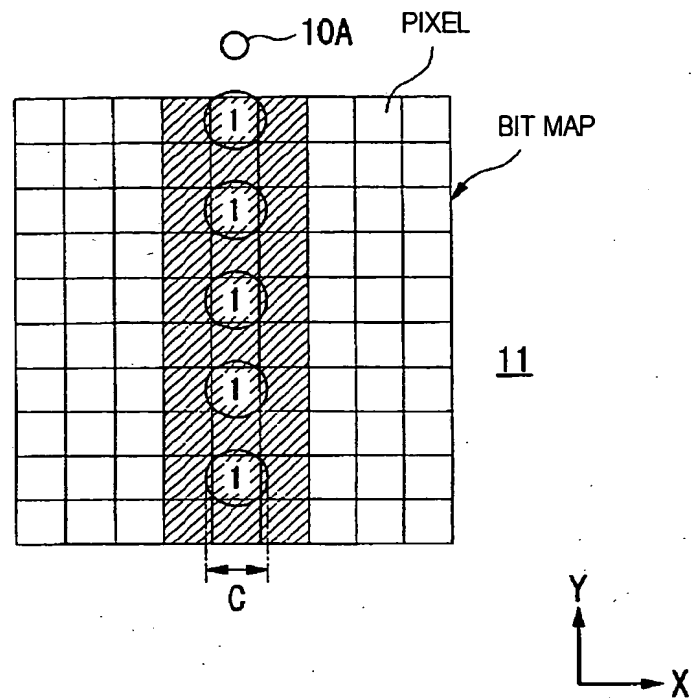


FIG. 4 B

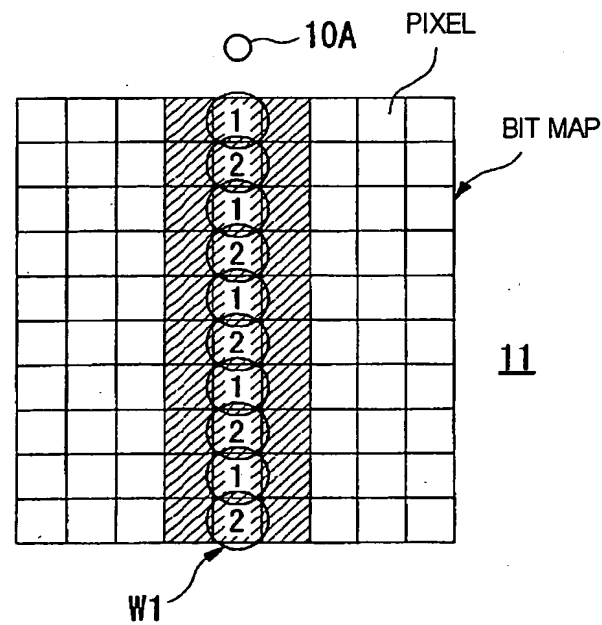


Figure 1 is a schematic diagram of a BIT MAP 11. The BIT MAP is a grid of pixels. A vertical column of pixels is highlighted with diagonal hatching and labeled W1. Each pixel in this column is divided into two sub-pixels, labeled 1 and 2. The sub-pixel 1 is further divided into three sub-sub-pixels, labeled 3, 1, and 2. A label PIXEL points to a pixel in the grid. A label 10A points to a small circle. A coordinate system with X and Y axes is shown at the bottom right.

[illegible]

FIG. 6 A

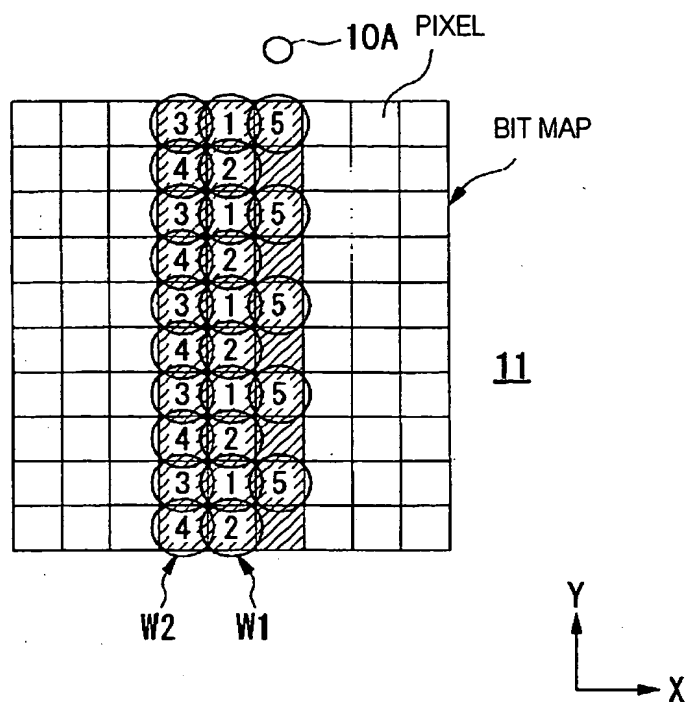


FIG. 6 B

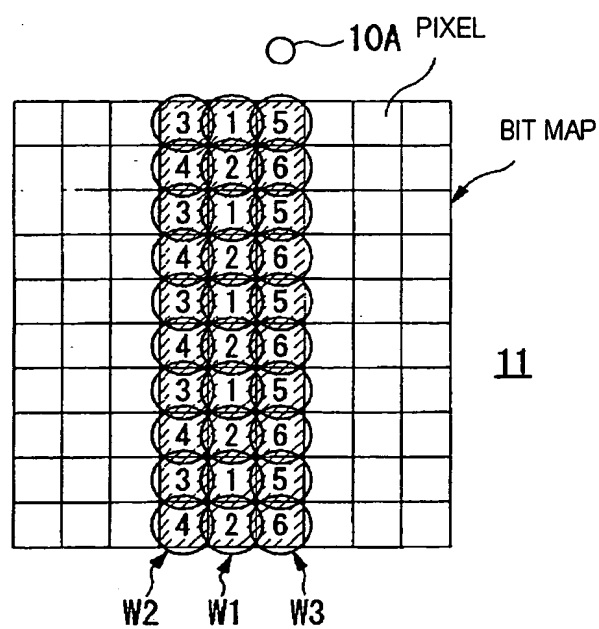


FIG. 7

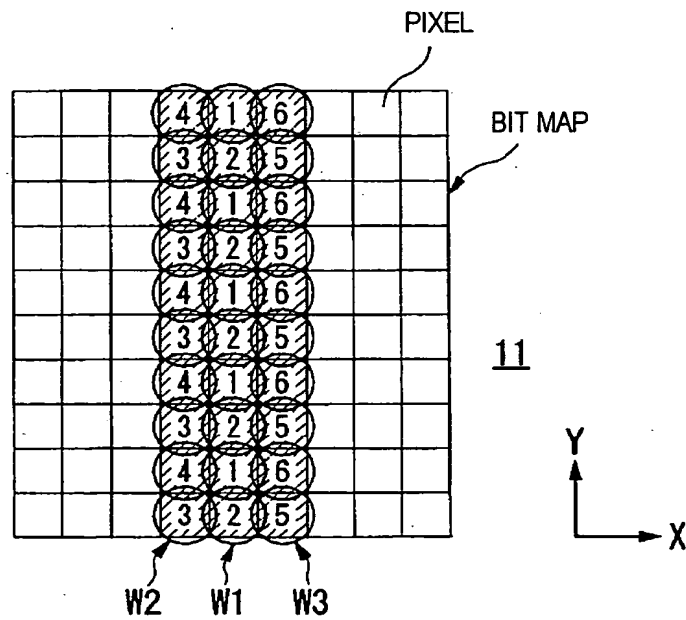


FIG. 8

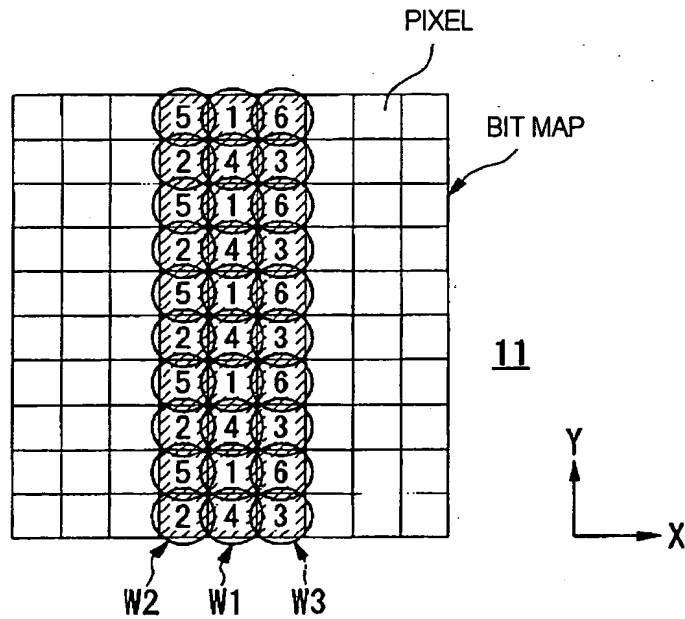


FIG. 9 A

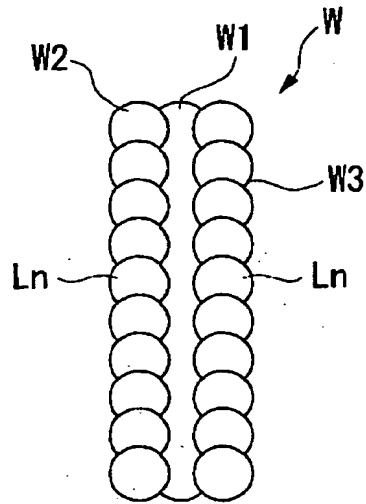


FIG. 9 B

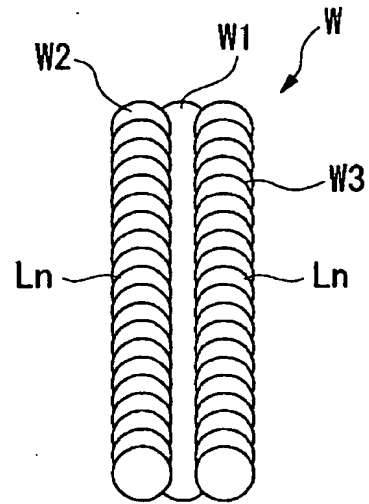


FIG. 10

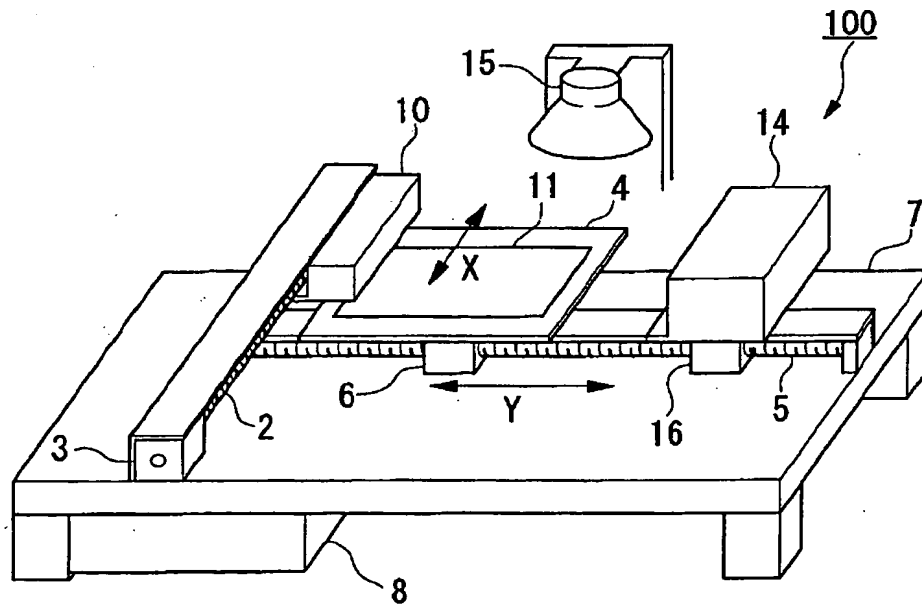




FIG. 11

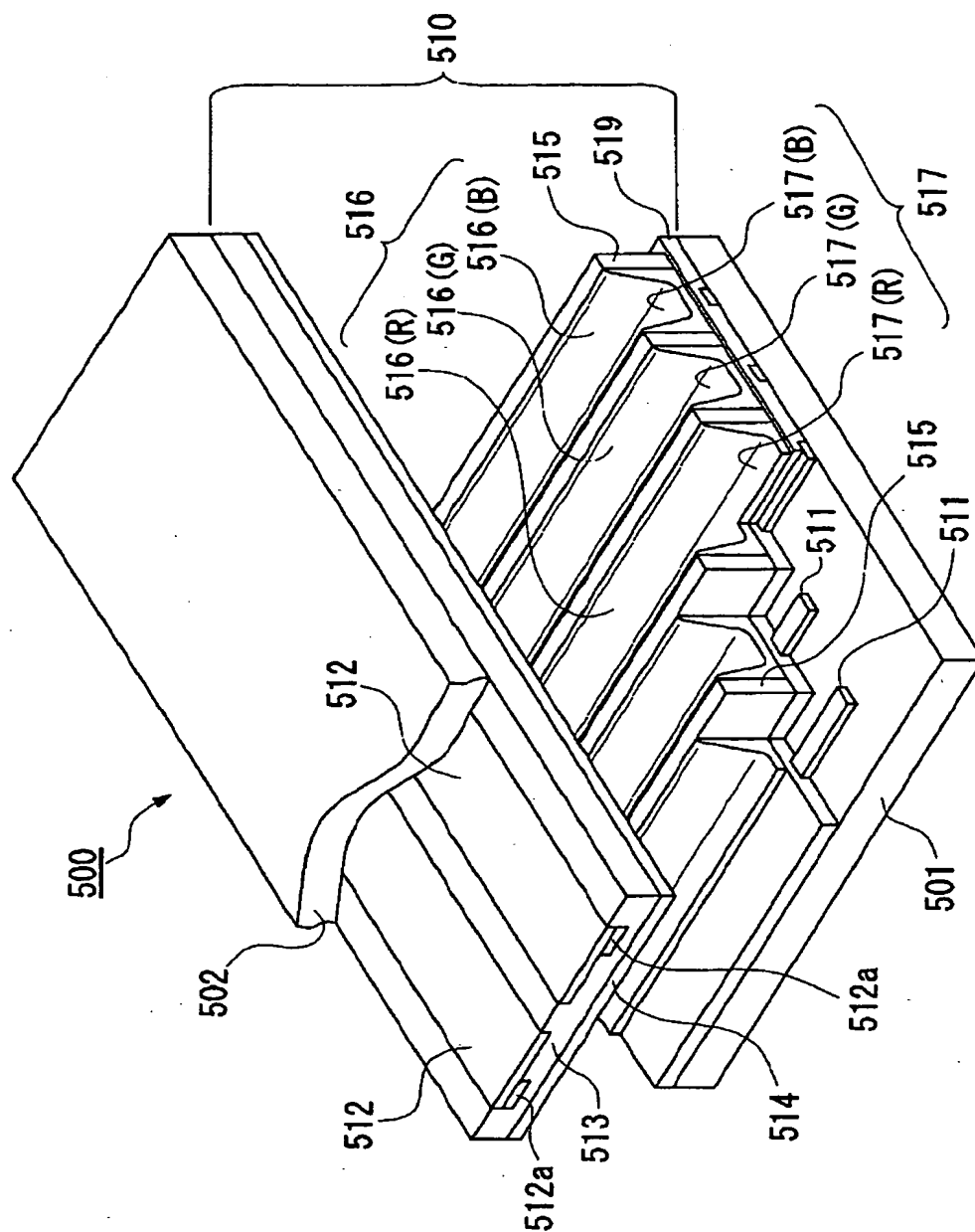


FIG. 12

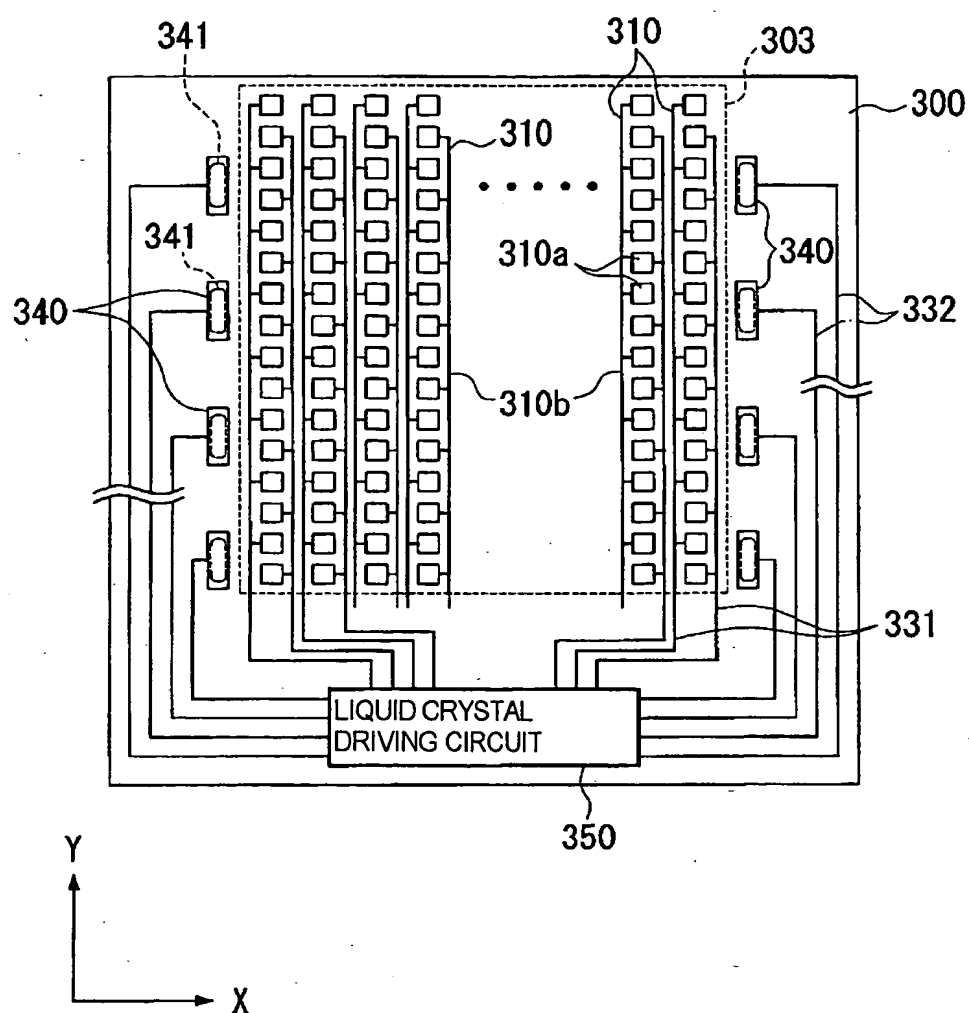
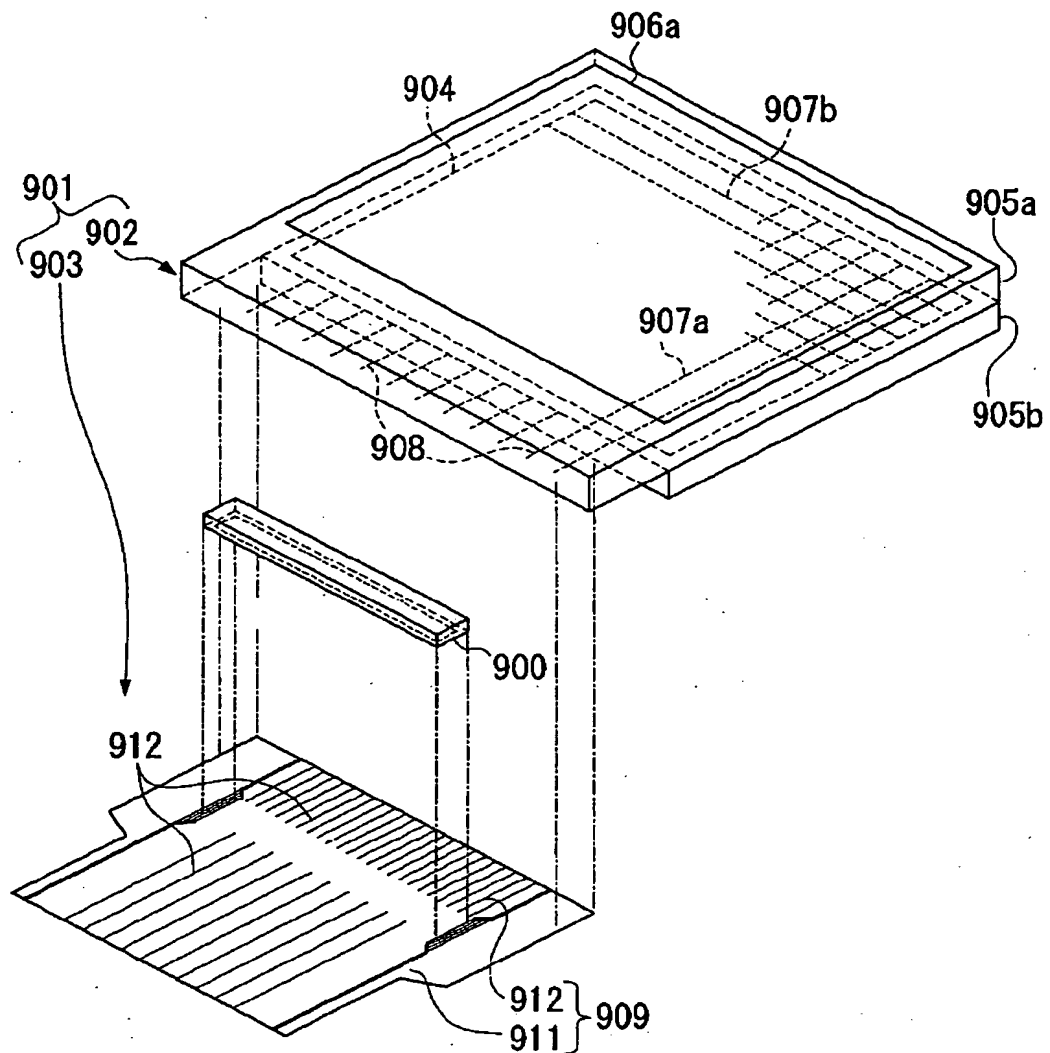


FIG. 13



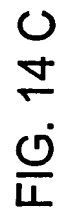
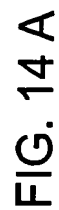
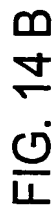


FIG. 15

